

# Mitsubishi Electric Corporation

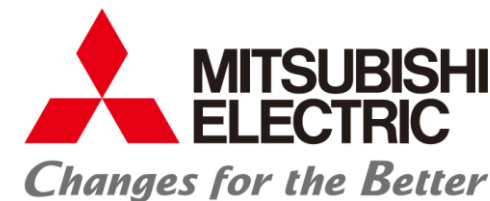
## Designated communication LSI for Master/Local station

### CP610

1. CC-Link IE TSN master/local stations can be developed without consideration of protocols.
2. The MPU and OS can be selected as needed, and sample code is provided that can be customized according to the selected hardware specifications and application.\*1
3. The CC-Link IE TSN configuration tool included in the source code development kit can be used to configure parameter settings and run diagnostics on CC-Link IE TSN master/local stations.
4. As a transmission line route simulation model, a SPICE model is available for PCI Express-I/F, and an IBIS model is available for other I/F.\*2

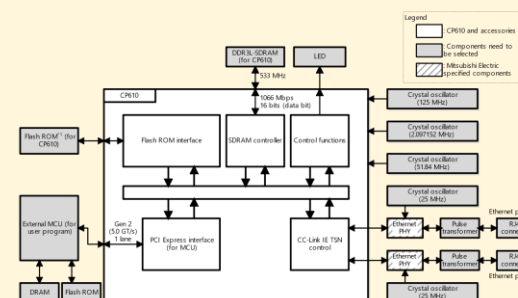
\*1 Sample code is provided for when the PCI Express® bus is used as the connection interface for the CP610 and an external MPU, and for when a parallel bus is used.

\*2 Conclusion of a confidentiality agreement is required in order to receive the SPICE model or IBIS model. Please contact a branch office or the Open System Center.

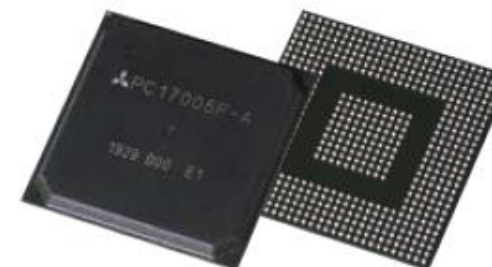
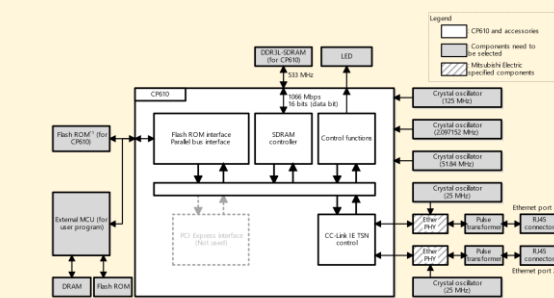


### Block Diagram

When connecting the CP610 and external MPU using the PCI Express® bus



When connecting the CP610 and external MPU using a parallel bus



CP610

URL: <https://www.mitsubishielectric.co.jp/fa/>

E-mail: [OSC@rj.MitsubishiElectric.co.jp](mailto:OSC@rj.MitsubishiElectric.co.jp)

# Mitsubishi Electric Corporation

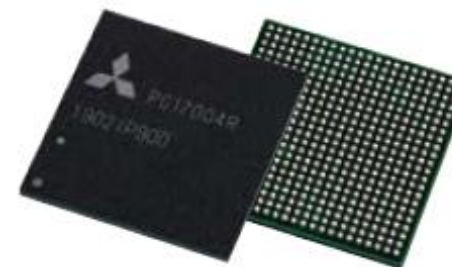
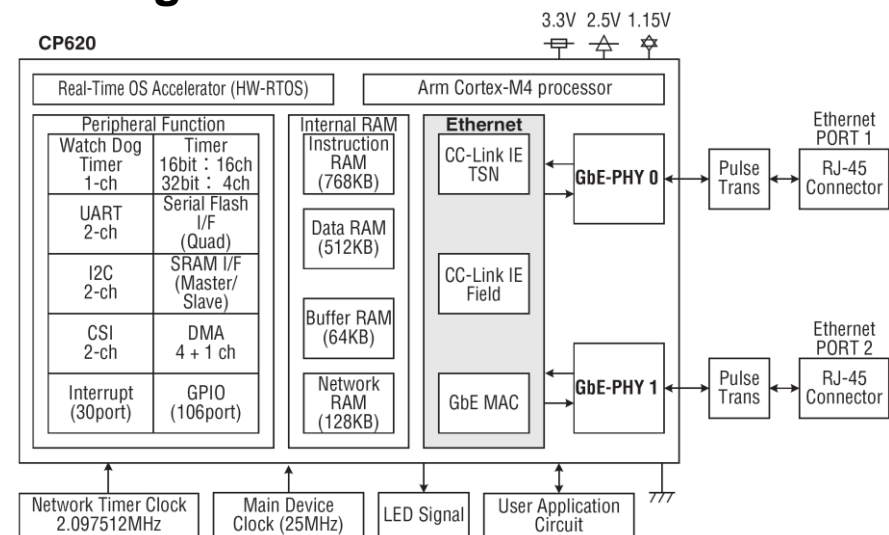
## Communication LSI with Built-in GbE-PHY for development of CC-Link IE TSN Remote Station

### CP620

1. CC-Link IE TSN remote stations and CC-Link IE Field Network intelligent device stations and remote device stations can be developed without consideration of protocols.
2. The inclusion of the GbE-PHY makes it easier to design communication circuit patterns. In addition, only a small number of peripheral components and circuits are required for the CPU and GbE-PHY, enabling development of more compact circuit boards.
3. The provided sample code can be customized to suit the applicable hardware specifications and applications.
4. The included H/W-RTOS reduces the CPU load and enables a lower power consumption in the developed equipment.



### Block Diagram



CP620

URL: <https://www.mitsubishielectric.co.jp/fa/>  
 E-mail: [OSC@rj.MitsubishiElectric.co.jp](mailto:OSC@rj.MitsubishiElectric.co.jp)